

Register No.:

733

April 2023

*Time – Three hours
(Maximum Marks: 100)*

- N.B.**
1. Answer all questions under Part-A. Each question carries 3 marks.
 2. Answer all the questions either (A) or (B) in Part-B. Each question carries 14 marks.

PART – A

1. What is control function?
2. List the components of CPU.
3. Define asynchronous data transfer.
4. What is IOP?
5. What are the characteristics of memory hierarchy?
6. Define write back in cache memory.
7. Define microprocessor.
8. List the types of parallel processing systems.
9. What is symmetric multiprocessor?
10. What are the various approaches to vector computation?

PART – B

11. A) (i) With a diagram, explain 4-bit arithmetic circuit. (7)
(ii) Explain indirect cycle in detail. (7)
(OR)
B) Explain the architecture of CISC with a neat block diagram.

[Turn over...

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12. A) Explain handshaking method of data transfer in detail.

(OR)

B) Explain the function of DMA transfer with block diagram

13. A) Explain (i) Optical disk (ii) SD card.

(OR)

B) (i) Explain in detail about set associative mapping method of cache memory. (7)

(ii) Explain page replacement techniques in detail. (7)

14. A) Explain the block diagram of 8086 architecture.

(OR)

B) (i) Explain about super scalar processor. (7)

(ii) Explain SIMD array processor with neat block diagram. (7)

15. A) Draw SMP organization and explain in detail.

(OR)

B) Explain the organization and features of i7 processor.
